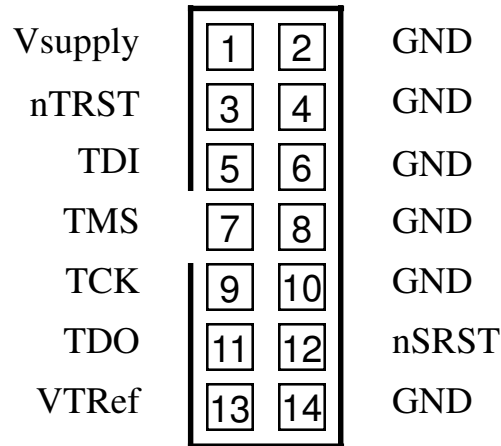


## Flyswatter - 14 Pin JTAG Connections



TOP VIEW

| Signal  | I/O    | Description   |
|---------|--------|---|
| Vsupply | Power  | Target supply voltage. Connect this pin to the target's main power supply. (Valid target voltage range is 1.2VDC to 3.3VDC)   |
| nTRST   | Output | Test Reset. This signal resets the TAP controller's state machine.  |
| TDI     | Output | Test Data In. This data is shifted into the target device. This pin should be pulled up on the target board.  |
| TMS     | Output | Test Mode State. This signal determines the next state of the TAP controller's state machine. This pin should be pulled up on the target board.                                 |
| TCK     | Output | Test Clock. This clock synchronizes the TAP controller's internal state machine operations. This pin should be pulled up on the target board.                                   |
| TDO     | Input  | Test Data Out. This data is shifted out of the target device into the Flyswatter. This pin should be pulled up on the target board.   |
| VTRef   | Power  | Target reference voltage. Connect this pin to the target's main power supply. (Valid target voltage range is 1.2VDC to 3.3VDC)  |
| nSRST   | Output | Target System Reset. Open collector output that resets the target system. Connect this pin to the target's main reset signal. This pin should be pulled up on the target board. |
| GND     | Power  | Ground. All GND pins should be connected to the target's system ground to improve noise immunity.   |